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# An Efficient Matching Algorithm for Acyclic Regular Expressions with Bounded Depth

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**Abstract.** In this paper, we study the regular expression matching problem for a subclass of regular expressions of small depth. A regular expression is *acyclic* if it is over the basis  $\Sigma \cup \{\cdot, |\}$ . By extending the SHIFT-AND approach by Wu and Manber (JACM 39(2),1992), we present an efficient algorithm that solves the regular expression matching problem for acyclic regular expressions with length  $m$  and depth  $d$  and an input text of length  $n$  in  $O(nmd/w)$  time using  $O(md)$  preprocessing and  $O(md/w)$  space in words on unit-cost RAM model with word length  $w$ . When the depth  $d$  is constant, typical in real applications, our algorithm runs in  $O(nm/w)$  time and  $O(m/w)$  space, while the algorithm by Bille (ICALP, 2006) runs in  $O(nm \log w/w)$  time and  $O(m \log w/w)$  space. Hence, this achieves  $O(\log m)$  and  $O(\log w)$  speed-ups in *small automata* ( $m \leq w$ ) and in *large automata* ( $m > w$ ) cases, resp., still keeping  $O(m/w)$  space.

## 1 Introduction

Recent emergence of massive text and sequence data in networks has attracted much attention to researches on efficient string processing technologies [1, 3, 4, 6, 7, 9, 12, 13]. In this paper, we study the *regular expression matching problem*, which is one of the most important problems in string processing. This problem is stated as follows: Given a regular expression  $R$  of length  $m$  and an input text  $T$  of length  $n$ , to determine if there exists some substring of  $T$  that belongs to the language defined by  $R$ .

For the regular expression matching problem, in 1968, Thompson [11] devised an efficient algorithm using the idea of simulating a special NFA called a TNFA (Thompson NFA), which is obtained from an input regular expression by a transformation (the 2nd column of Fig. 1). This gives an  $O(nm)$  time and  $O(m)$  space algorithm for the problem.

In 1992, Myers [6] gave the first improvement of  $O(nm)$  time complexity of Thompson's algorithm by presenting an  $O(nm/w) = O(nm/\log n)$  time algorithm on the  $(\log n)$ -uniform RAM model, where  $w = O(\log n)$  by using so-called *four Russians technique*, which is a combination of modular decomposition of a TNFA and its simulation using table lookup (the 3rd column of Fig. 1). The space complexity of the algorithm is  $O(nm/\log n)$  space (in words), which is still dependent of the text size  $n$ <sup>1</sup>.

Then, Bille [4] breaks the  $O(m)$  space barrier of space complexity by giving algorithms of  $O(nm \log w/w)$  time for large automata and  $O(n \log m)$  time for small automata using  $O(m)$  words space (the 4th column of Fig. 1). His algorithm is based on a hierarchical decomposition of a given TNFA to a balanced binary tree with  $O(\log m)$  depth, and simulates the TNFA level by level in a topdown manner using bit-parallelism with Boolean operations and subtraction.

In this paper, we study efficient regular expression matching for unbounded regular expressions with simple structure, large branching, and small depth, typically appearing

<sup>1</sup> For any fixed  $0 < \epsilon \leq 1$ , we can derive an algorithm with space  $O(m2^x) = O(m^{1+\epsilon})$  bits by setting the block length  $x = \epsilon \log m$ . However, its time complexity become  $O(\frac{1}{\epsilon}(nm/\log m))$  with factor  $\frac{1}{\epsilon}$ .

Algorithm	Pattern	Time	Preprocess	Space (in words)
Thompson [11]	$\text{REG}(\Sigma, \varepsilon, \cdot,  , *)$	$O(nm)$	$O(m)$	$O(m)$
Myers [6]	$\text{REG}(\Sigma, \varepsilon, \cdot,  , *)$	$O(nm/w)$	$O(nm/w)$	$O(nm/w)$
Bille [4]	$(m > w)$ $(m \leq w)$	$\text{REG}(\Sigma, \varepsilon, \cdot,  , *)$	$O(m \log w)$	$O(m \log w/w)$
			$O(n \log m)$	$O(m \log m)$
Ours Sec. 3	$(m > w)$ $(m \leq w)$	$\text{REG}(\Sigma, \cdot,  )$	$O(md)$	$O(md/w)$
			$O(nd)$	$O(d)$

**Fig. 1.** Summary of the results on regular expression matching algorithms in terms of time  $T$ , space  $S$  (in words), and preprocessing  $P$  for unbounded acyclic regular expressions over constant alphabets in text size  $n$ , pattern size  $m$ , pattern depth  $d$ , and word length  $w$ .

in practical applications such as NIDS [9, 10]. As a main result, for the class AREG of unbounded acyclic regular expressions over the basis of letters in  $\Sigma$ , dot '.', and union '|', we present an algorithm that solves the regular expression matching problem in  $O(nmd/w)$  time using  $O(md + m|\Sigma|/w)$  preprocessing and  $O(md/w + m|\Sigma|/w)$  space in words, where  $d \geq 0$  is the depth of a given regular expression. When the depth  $d$  is constant, this result gives an  $O(\log w)$  time speed up of the  $O(nm \log w/w)$  algorithm by Bille [4] still keeping  $O(m/w)$  space bound. The key of our algorithm is efficient simulation of the  $\varepsilon$ -closure on TNFA tailored for acyclic regular expressions with small depth. Since our TNFAs are of unbounded branching, we cannot directly apply Bille [4]'s simulation technique. Hence, we introduce new simulation techniques based on an extension of the SHIFT-AND bit-parallel technique [7, 12], called *scatter and gather operations* using word-level parallelism.

The advantages of our approach to regular expression matching are summarized as follows. (i) Simple and efficient: Since our algorithm naturally exploits the composition structure of TNFA and does not use complex module decomposition as in [4] (in the small automata case), it is particularly efficient for regular expressions with small depth. (ii) Hardware friendly: Since it uses only simple bit-operations and addition/subtraction and avoids the use of table-lookup, it has potential to be implemented on modern parallel hardwares with simple structure, such as GPGPUs or FPGAs. We can even implement these instructions directly on hardwired circuits.

The organization of this paper is as follows. In Section 2, we give basic definitions. In Section 3 and Section 4, we present our algorithm *BPD* (Algorithm with Bit-Parallel Distribution Techniques) for  $\text{AREG} = \text{REG}(\Sigma, \cdot, |)$ . In Section 5, we report our experimental results and, in Section 6, we conclude.

## 2 Preliminary

In this section, we give basic definitions and notations in the regular expression matching problem according to [4, 6]. For detailed definitions, please consult text books [2, 5, 7].

### 2.1 Regular expressions and the matching problem

**Basic definitions.** Let  $\Sigma$  be a finite alphabet of *letters*. A *string* on  $\Sigma$  is a sequence  $S = s_1 \cdots s_n$  of letters, where  $S[i] = s_i \in \Sigma$  for every  $1 \leq i \leq n$ . We denote by  $S[i..j]$  the substring  $s_i \cdots s_j$  for every  $i \leq j$ , and by  $\varepsilon$  the *empty string*. For a set  $S \subseteq \Sigma^*$  of strings, we denote by  $|S|$  the cardinality and by  $\|S\| = \sum_{s \in S} |s|$  the total size of  $S$ . We denote by  $\Sigma^*$  and  $\Sigma^+ = \Sigma^* \setminus \{\varepsilon\}$  the sets of all strings and all non-empty strings on  $\Sigma$ .

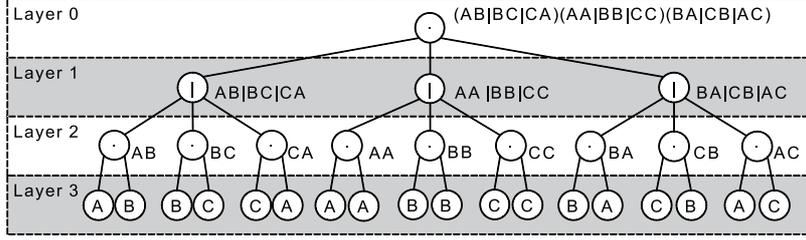


Fig. 2. The parse tree for an acyclic regular expression  $R_1 = (AB|BC|CA)(AA|BB|CC)(BA|CB|AC)$ .

**Regular expressions.** The class of *regular expressions* on  $\Sigma$  is defined recursively as follows: (1) If  $a \in \Sigma \cup \{\varepsilon\}$  then  $a$  is a regular expression. (2) If  $R_1, \dots, R_n$  are regular expressions then  $(R_1 \cdots R_n)$ ,  $(R_1 | \cdots | R_n)$ ,  $(R_1)^+$ , and  $(R_1)^*$  are regular expressions. In the above definition, regular expressions are *unbounded*, i.e.,  $n \geq 1$  can be any integer, while  $n = 2$  in the standard definition [2, 4, 7]. The *length* (or the *size*) of  $R$  is defined by the number of symbols of  $\Sigma \cup \{\varepsilon, \cdot, |, *, +\}$  appearing in  $R$ . A regular expression  $R$  defines the *language*  $L(R) \subseteq \Sigma^*$  in the standard way [2, 7].

**Acyclic regular expressions.** As notation, for any subset  $\mathcal{O} \subseteq \{\Sigma, \varepsilon, \cdot, |, *, +\}$ , we denote by  $\text{REG}(\mathcal{O})$  the subclass of  $\text{REG}$  of all regular expressions constructed from the letters and the operators in  $\mathcal{O}$ . Then, we denote by  $\text{REG} = \text{REG}(\Sigma, \varepsilon, \cdot, |, *) = \text{REG}(\Sigma, \varepsilon, \cdot, |, *, +)$  the class of regular expressions. In this paper, we introduce a subclass of  $\text{REG}$ , the class  $\text{AREG} = \text{REG}(\Sigma, \cdot, |)$  of *acyclic* regular expressions<sup>2</sup>. The *depth* of  $R$ ,  $d(R)$ , is defined by:  $d(a) = 0$  for  $a \in \Sigma \cup \{\varepsilon\}$ .  $d((R_1 \cdots R_n)) = d((R_1 | \cdots | R_n)) = 1 + \max_i d(R_i)$ , and  $d((R_1)^+) = d((R_1)^*) = 1 + d(R_1)$ . For every  $d \geq 0$ , we denote by  $\text{AREG}^d$  the subclass consisting only of expressions with depth no more than  $d$ .

*Example 1.*  $R_1 = ((AB|BC|CA)(AA|BB|CC)(BA|CB|AC)) \in \text{AREG}^3$  is an acyclic regular expression with depth  $d = 3$ .  $R_2 = (A|B|C|D) \in \text{AREG}^1$  and  $R_3 = ((A|B)|(C|D)) \in \text{AREG}^2$  are acyclic regular expression with depth 1 and depth 2, respectively.

**Parse trees.** The *parse tree* for a regular expression  $R$  is a labeled, rooted, and ordered tree  $T_R$  with node set  $\text{dom}(T_R)$  satisfying: (1) The leaves are labeled with symbols  $\text{lab}(v)$  from  $\Sigma \cup \{\varepsilon\}$ . (2) The internal nodes are labeled with operators  $\text{lab}(v)$  from  $\{\cdot, |, *, +\}$ . (3) The internal nodes labeled with  $*$  or  $+$  have one child, and those labeled with  $\cdot$  and  $|$  have unbounded number of children. We denote the *depth* of node  $v$  by  $d(v)$  and the *depth* of  $T_R$  by  $d(T_R)$ . Parse trees have one-to-one correspondence to regular expressions in a standard manner [5]. Fig. 2 shows an example of the parse tree  $T_{R_1}$  for  $R_1$  in Example 1.

**Non-deterministic finite automata.** We use non-deterministic finite automata for recognizing the strings in a regular language. A *non-deterministic finite automaton (NFA)*, for short) is a quadruple  $N = (V, E, \theta, \phi)$ , consists of the followings: (1) A set  $V$  of nodes, called *states*. (2) A set  $E \subseteq V \times (\Sigma \cup \{\varepsilon\}) \times V$  of labeled, directed *edges*. (3) Distinguished nodes  $\theta$  and  $\phi \in V$ , called the *source* and the *sink* of  $N$ . Each element  $(x, a, y) \in E$ , where  $a \in \Sigma \cup \{\varepsilon\}$ , is called an *a-edge* or a *a-transition* from state  $x$  to state  $y$ . We define a *path* as a connected sequence  $\pi$  of edges. A path  $\pi$  *spells* a string  $s$  if  $s$  is obtained by concatenating the labels of the edges in  $\pi$ . The *language accepted by*  $N$  is the set  $L(N) \subseteq \Sigma^*$  of all strings spelled on the paths from the source  $\theta$  to the sink  $\phi$  in  $N$ .

<sup>2</sup> Note that this is different from the notion of *acyclic regular languages* [8], which is the class of languages defined by *extended* regular expressions without “ $*$ ” and “ $+$ ”, but with complement  $\bar{\phantom{x}}$ .

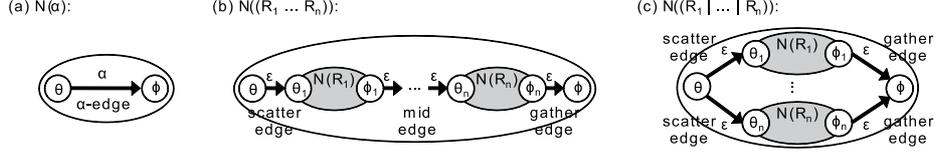


Fig. 3. Construction of Thompson automata (TNFAs).

## 2.2 Regular expression matching problem

A *pattern* is any regular expression and a *text* of length  $n$  is a string  $T$  over  $\Sigma$ . We say that a regular expression  $R$  *occurs* in a text  $T$  if there exist some  $i \leq j$  such that  $T[i..j] \in L(R)$  holds. Then, the index  $j$  is called the *end position* of  $R$  in  $T$ . Let  $\mathcal{C} \subseteq \text{REG}$  be any subclass of REG. Now, we state our problem below.

**Definition 1.** *The regular expression matching problem for  $\mathcal{C}$  is, given a regular expression  $R \in \mathcal{C}$  of length  $m$  and a text  $T = t_1 \cdots t_n$  of length  $n$ , to output the set of all end positions of  $R$  in  $T$ .*

As the model of computation, we assume the *uniform-cost RAM* with word length  $w \geq \log n$  [5]. Let  $M \geq 0$  be a positive integer. A *bitmask* is a vector of  $M$  bits  $X = b_M \cdots b_1 \in \{0, 1\}^M$ . If  $M \leq w$  we say the *small automata case*, and if  $M > w$  we say the *large automata case* [4]. For a bit  $b \in \{0, 1\}$ ,  $b^k$  denotes the bitmask consisting of  $m$  copies of  $b$ . We assume that for bitmasks with  $M \leq w$ , Boolean and arithmetic operations *bitwise-and* “&”, *bitwise-or* “|”, *bitwise-not* “~”, *left-shift* “<<”, *right-shift* “>>”, *addition* “+” and *subtraction* “-” are executed in  $O(1)$  time. The space complexity is measured in the number of computer words.

## 3 Efficient State-set Simulation of Thompson Automata

In this section, we review Thompson’s algorithm for regular expression matching based on the state-set simulation of a special class of automata, called TNFAs. Next, we discuss how to speed-up the simulation of TNFA.

### 3.1 Construction of Thompson automata

Thompson [11] gave a method to construct a NFA in a special form, called a TNFA, from a regular expression  $R$  that exactly accepts the language  $L(R)$ . The classification of  $\varepsilon$ -edges into scatter, gather, mid, and back edges below is not contained in the original [11] and newly introduced in this paper.

Let  $R \in \text{AREG}$  be any acyclic regular expression and  $T_R$  be the parse tree for  $R$ . For each node  $v$ , let  $R(v)$  be the regular expression that corresponds to the subtree  $T_R(v)$  rooted at  $v$ . The following method recursively constructs the TNFA  $N(v) = N_R = (V(v), E(v), \theta(v), \phi(v))$  for each node  $v$  by traversing the parse tree  $T_R$  in the post-order. During the traversal, we construct the following auxiliary information for each node  $v$ : the source  $\theta(v)$  and the sink  $\phi(v)$ ; the sets of edges  $LE(v)$ ,  $SE(v)$ ,  $GE(v)$ , and  $ME(v)$  whose members are called *letter*, *scatter*, *gather*, and *mid* edges; First, we suppose that  $v$  is a leaf of  $T_R$ .

- (a) For  $R(v) = \alpha \in \Sigma$  with  $\text{lab}(v) = \alpha$ , we build TNFA  $N(v) = N_\alpha = (\{\theta, \phi\}, LE(v), \theta, \phi)$  by connecting  $\theta = \theta(v)$  and  $\phi = \phi(v)$  with letter-edge in  $LE(v) = \{(\theta, \alpha, \phi)\}$  as in (a) of Fig 3.

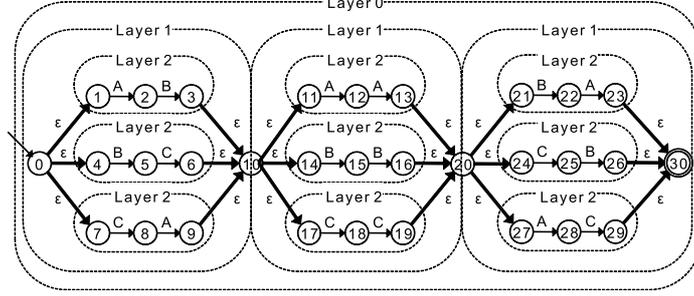


Fig. 4. The TNFA for an acyclic regular expression  $R_1 = (AB|BC|CA)(AA|BB|CC)(BA|CB|AC) \in \text{AREG}$ .

Next, we suppose that  $v$  is an internal node with  $n$  children  $v_1, \dots, v_n$  for  $n \geq 1$ . For every  $1 \leq i \leq n$ , we assume that a TNFA  $N_{R_i} = (V_i, E_i, \theta_i, \phi_i)$  with source  $\theta_i = \theta(v_i)$  and sink  $\phi_i = \phi(v_i)$  is already built for  $R_i = R(v_i)$ .

- (b) For concatenation  $R(v) = (R_1 \cdots R_n)$  with  $\text{lab}(v) = \cdot$ , we build TNFA  $N(v) = N_{(R_1 \cdots R_n)}$  by adding new source  $\theta = \theta(v)$  and sink  $\phi = \phi(v)$ , and by connecting subautomata  $R_1, \dots, R_n$  in serial by edges in  $SE(v) = \{(\theta, \varepsilon, \theta_1)\}$ ,  $ME(v) = \{(\phi_i, \varepsilon, \theta_{i+1}) \mid 1 \leq i \leq n-1\}$ , and  $GE(v) = \{(\phi_n, \varepsilon, \phi)\}$  as in (b) of Fig 3.
- (c) For union  $R(v) = (R_1 | \cdots | R_n)$  with  $\text{lab}(v) = |$ , we build TNFA  $N(v) = N_{(R_1 | \cdots | R_n)}$  by adding new source  $\theta = \theta(v)$  and sink  $\phi = \phi(v)$ , and by connecting subautomata  $R_1, \dots, R_n$  in parallel by edges in  $SE(v) = \{(\theta, \varepsilon, \theta_i) \mid 1 \leq i \leq n\}$  and  $GE(v) = \{(\phi_i, \varepsilon, \phi) \mid 1 \leq i \leq n\}$  as in (c) of Fig 3.
- (d) In the above definition, if any of the sets  $LE(v)$ ,  $SE(v)$ ,  $GE(v)$ ,  $ME(v)$ , and  $BE(v)$  is undefined, then we define that it is empty. We also define  $\Psi(v) = \{\theta(v), \phi(v)\}$  and  $\Delta(v) = LE(v) \cup SE(v) \cup GE(v) \cup ME(v) \cup BE(v)$ . Then, we define the node set by  $V(v) = \Psi(v) \cup (\bigcup_{i=1}^n \Psi(v_i))$  and the edge set by  $E(v) = \Delta(v) \cup (\bigcup_{i=1}^n E_i)$ .

In Fig. 4, we show the TNFA for the acyclic regular expression  $R_1$  of Example 1.

### 3.2 Efficient State-set Simulation

Most of the previous regular expression matching algorithms [3, 4, 6, 7, 12] employ the standard *state-set simulation* method developed by Thompson [5, 11]. In this method, an algorithm first builds TNFA  $N_R$  of a given regular expression  $R$ , and then simulates the computation by  $N_R$  on text  $T$  using an appropriate data structure.

Let  $R$  be a regular expression, and let  $N_R = (V_R, E_R, \theta_R, \phi_R)$  be the TNFA for  $R$  constructed in Section 3.1. In Thompson’s state-set simulation [11], the current status of the TNFA  $N_R$  is represented by a set  $S \subseteq V_R$  of *active states*. Let  $N = N_R$  be the TNFA and  $V = V_R$  be its state set. For any set  $S \subseteq V$  of active states and any letter  $\alpha \in \Sigma$ , we define the following operations:

- $\text{Init}_N$  returns the initial state set  $\{\theta_R\}$  and  $\text{Accept}_N$  returns the sink state set  $\{\phi_R\}$ .
- $\text{Move}_N(S, \alpha)$  returns the set of all states that is reachable from a current active state by a single  $\alpha$ -transition in  $E$ . That is,  $\text{Move}_N(S, \alpha) = \{y \in V \mid x \in S, (x, \alpha, y) \in E_R\}$ .
- $\text{EpsClo}_N(S)$  returns the set of all states that is reachable from a current active state by one or more  $\varepsilon$ -transitions in  $E$ . That is,  $\text{EpsClo}_N(S) = \{y \in V \mid x \in S, (x_1, \varepsilon, x_2), (x_2, \varepsilon, x_3), \dots, (x_{n-1}, \varepsilon, x_n) \in E_R\}$ , and called the  $\varepsilon$ -closure of  $S$ .

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<pre> <i>algorithm</i> RunTNFA(<math>T = t_1 \cdots t_n</math>: a text, <math>N</math>: a TNFA) 1: <math>X \leftarrow \text{Init}_N</math>; //The initial state set 2: <math>X \leftarrow \text{EpsClo}_N(X)</math>; //Simulate <math>\varepsilon</math>-transition 3: <b>for</b> <math>i \leftarrow 1, \dots, n</math> <b>do</b> 4:   <math>X \leftarrow \text{Move}_N(X, t_i)</math>; //Simulate <math>\alpha</math>-transition 5:   <math>X \leftarrow \text{EpsClo}_N(X)</math>; //Simulate <math>\varepsilon</math>-transition 6:   <b>if</b> <math>X \cap \text{Accept}_N \neq \emptyset</math> <b>then</b> 7:     <i>output</i> <math>\text{Match}</math>; //A match found 8: <b>end for</b> </pre>	<pre> <i>procedure</i> EpsClo(<math>S</math>: state set): 1: <b>for</b> <math>k \leftarrow d(R), \dots, 0</math> <b>do</b> <math>S \leftarrow \text{Apply}[GE[k]](S)</math>; 2: <b>for</b> <math>k \leftarrow d(R), \dots, 0</math> <b>do</b> <math>S \leftarrow \text{Apply}[ME[k]](S)</math>; 3: <b>for</b> <math>k \leftarrow 0, \dots, d(R)</math> <b>do</b> <math>S \leftarrow \text{Apply}[SE[k]](S)</math>; 4: <b>return</b> <math>X</math>; </pre>
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**Fig. 5.** The algorithm RunTNFA for scanning phase.

**Fig. 6.** The algorithm EpsClo for computing  $\varepsilon$ -closure for acyclic regular expressions

In Fig. 5, we show an algorithm RunTNFA that simulates the computation of TNFA  $N$  on an input text  $T$ . We can show that the following lemma [11].

**Lemma 1 (Thompson [11]).** *For any input text  $T = t_1 \cdots t_n$ , the algorithm RunTNFA in Fig. 5 correctly solves the regular expression matching problem for REG.*

Efficient implementation of  $\text{Move}_N$  is not hard either by using *table lookup* [6] or using *SHIFT-AND* bit-parallel technique [12]. However, an efficient implementation of  $\text{EpsClo}_N$  is not straightforward since we have to compute the transitive closure of  $\varepsilon$ -edges.

The key of our algorithm is an efficient implementation of  $\text{EpsClo}_N$  by hierarchical decomposition. We partition the  $\varepsilon$ -edges of TNFA  $N_R$  into a collection of mutually disjoint layers as follows. Let  $V_R = V[1] + \cdots + V[k]$  be the partition of the node set  $V_R$  by their depths, where  $V[k] = \{v \in V_R \mid d(v) = k\}$  for every level  $1 \leq k \leq d(T_R)$ .  $V[k]$  is called the  $k$ -th layer of  $V_R$ . We define the sets  $LE[k]$ ,  $SE[k]$ ,  $GE[k]$ ,  $ME[k]$ , and  $BE[k]$  for level  $k$  as follows. For  $Z \in \{LE, SE, GE, ME, BE\}$ , the  $k$ -th layer  $Z[k]$  of the edge set  $Z$  is define by  $Z_k = \{e \mid e \in Z(v), v \in V[k]\}$ . For any subset  $F \subseteq E_R$  of  $\varepsilon$ -edges and any set  $S \subseteq V_R$  of active states, we define the operation  $\text{Apply}[F](S)$  by the set of all states reachable from a current active state by zero or one  $\varepsilon$ -transition in  $F$ . In Fig. 6, we present an algorithm that computes  $\varepsilon$ -closure for acyclic regular expressions in  $\text{AREG} = \text{REG}(\Sigma, \varepsilon, \cdot, |)$ .

**Lemma 2.** *Let  $R$  be any unbounded acyclic regular expression with depth  $d = d(R)$  and  $N_R$  be its TNFA. For any state set  $S \subseteq V_R$ , the algorithm EpsClo of Fig. 6 correctly computes the  $\varepsilon$ -closure  $\text{EpsClo}(S)$  of  $S$  on  $N_R$ .*

*Proof.* We can see that if  $R$  is acyclic then for any subexpression  $R'$  with source  $\theta'$  and sink  $\phi'$ , there is no  $\varepsilon$ -path from  $\theta'$  to  $\phi'$ . Furthermore, we can see that if  $\pi$  is any  $\varepsilon$ -path in the TNFA then it has the form  $\pi = (g_p, g_{p-1}, \dots, g_r, m_r, s_r, s_{r+1}, \dots, s_q)$ , first going up by gather edges  $g_p, g_{p-1}, \dots, g_r \in GE[k]$ , reaching a mid edge  $m_r \in ME[r]$ , and then going down by scatter edges  $s_r, s_{r+1}, \dots, s_q \in SE[k]$ , such that their depths satisfies  $0 \leq r \leq \min\{p, q\} \leq d(R)$ . Then, we see that EpsClo simulates the transition along  $\pi$ .  $\square$

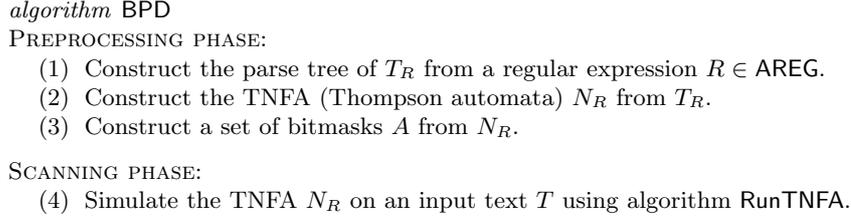
## 4 An efficient algorithm for acyclic regular expressions

### 4.1 Outline of the algorithm

In this section, we present an algorithm BPD (*Bit-Parallel Algorithm using Bit-Distribution*) that efficiently solves the regular expression matching problem for acyclic regular expressions in AREG. For an acyclic regular expression  $R \in \text{AREG}$  with length  $m$  and depth  $d$  and

a text  $T$  with length  $n$ , this algorithm runs in  $O(nmd/w)$  time using  $O(md)$  preprocessing and  $O(md/w)$  space in words on unit-cost RAM with word length  $w$ .

Our matching algorithm BPD consists of a preprocessing phase that transform an input regular expression  $R \in \text{AREG}$  into a number of tables followed by a scanning phase that search for the occurrences of  $R$  in an input text  $T$  in the following steps.



**Fig. 7.** The outline of our regular expression matching algorithm BPD

## 4.2 Preprocessing phase: Computation of bitmasks

In this subsection, we will describe the preprocessing phase. The key of our algorithm is efficient bit-parallel simulation of  $\varepsilon$ -closure  $\text{EpsClo}_A(X)$ . Given a regular expression  $R$ , the preprocessing phase first builds the parse tree  $T_R$ , and then the TNFA  $N_R$  from  $T_R$ . The algorithm then builds a set of bitmasks from  $N_R$  using the construction described below.

**Numbering of states.** For the TNFA  $N_R$  with  $M$  states, our algorithm uses  $M$ -bit integer  $X \in \{0, 1\}^M$  as a bitmask with length  $M$  to encode any subset of  $V_R$ . To do this, we assign a unique bit-position  $1 \leq \text{Bit}(x) \leq M$  to each state  $x \in V_R$  by the depth-first traversal of  $D_R$ , where the source  $\theta(v)$  and the sink  $\phi(v)$  are numbered at node  $v$  in the pre-order and the post-order, respectively. For any state set  $S \subseteq V_R$ , define  $\text{BIT}(S) = \{\text{Bit}(x) \mid x \in S\}$ . For any subset  $X \subseteq [1, M]$ , define  $\text{NUM}(X)$  to be the bitmask that represents  $X$ . Then, it is ensured that for each node  $v$ , the states subautomata  $N(v)$  occupies a continuous interval  $I(v) = [\text{Bit}(\theta(v)), \text{Bit}(\phi(v))] \subseteq [1, M]$ , in a bitmask. For distinct  $u, v \in V[k]$  in the same level  $k$ , their intervals do not overlap, i.e.,  $I(u) \cap I(v) = \emptyset$ .

**Construction of bitmasks.** For any acyclic regular expression  $R \in \text{AREG}$  with depth  $d = d(R) = d(T_R)$  and its TNFA  $N_R$ , we construct a number of bitmasks in the preprocessing phase as follows.

- (1) *Alpha-edge transition:* For  $\alpha \in \Sigma$ ,
 
$$\text{Chr}[\alpha] = \bigvee_{v \in \text{dom}, \text{lab}(v) = \alpha} \text{NUM}(\text{Bit}(\phi(v))).$$
- (2) *Scatter-edge transition:* For  $k \in [0, d]$ ,
 
$$\begin{aligned} \text{SSrc}[k] &= \bigvee_{v \in V[k]} \text{NUM}(\text{Bit}(\theta(v))). \\ \text{SDst}[k] &= \bigvee_{v \in V[k], (\theta = \theta(v), \varepsilon, \theta') \in \text{SE}(v)} \text{NUM}(\text{Bit}(\theta')). \\ \text{SBlk}[k] &= \bigvee_{v \in V[k]} \text{NUM}(\text{Bit}(\phi(v))). \end{aligned}$$
- (3) *Gather-edge transition:* For  $k \in [0, d]$ ,
 
$$\begin{aligned} \text{GSrc}[k] &= \bigvee_{v \in V[k], (\phi', \varepsilon, \phi = \phi(v)) \in \text{GE}(v)} \text{NUM}(\text{Bit}(\phi')). \\ \text{GDst}[k] &= \bigvee_{v \in V[k]} \text{NUM}(\text{Bit}(\phi(v))). \\ \text{GBlk}[k] &= \bigvee_{v \in V[k]} \text{NUM}([\text{Bit}(\theta(v)), \text{Bit}(\phi(v)) - 1]). \end{aligned}$$

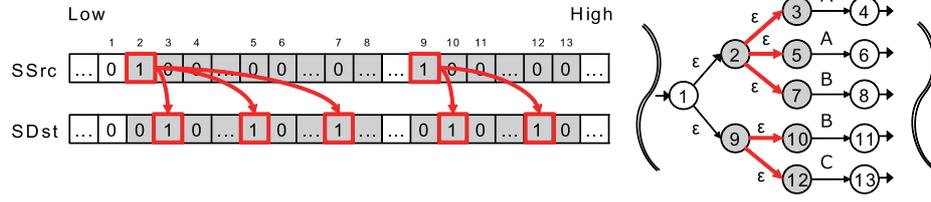


Fig. 8. Scatter transitions.

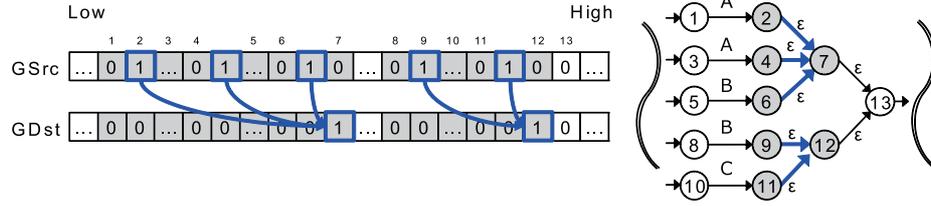


Fig. 9. Gather transitions.

- (4)
- Mid-edge transition*
- : For
- $k \in [0, d]$
- ,

$$Mid[k] = |\{v \in V[k], (\theta', \varepsilon, \phi') \in ME(v) \mid NUM(Bit(\phi'))\}|.$$

### 4.3 Scanning phase: Simulating TNFA in bit-parallel computation

The scanning phase simulate the behavior of the TNFA  $N_R$  in a standard state-set simulation by bit-parallel computation using previously constructed bitmasks. This is done as follows. Let  $S$  be any state set and  $X = NUM(BIT(S))$  be the corresponding bitmask. Then, we define a number of procedures for state transition as follows.

- (1)
- Alpha-edge transition*
- : For
- $\alpha \in \Sigma$
- , define
- $\text{Alpha}(X, \alpha)$
- by

$$X \leftarrow (X \ll 1) \& \text{Chr}[\alpha] \quad (1)$$

This is same as the SHIFT-AND method for exact string matching [3, 12]. For any letter-edge  $(x, \alpha, y) \in LE(v)$ ,  $Bit(y) = Bit(x) + 1$  by construction. Thus, the above code correctly implements  $\text{Move}(S)$ .

- (2)
- Scatter-edge transition*
- : For
- $k \in [0, d]$
- , define
- $\text{Scatter}(X, d)$
- by

$$X \leftarrow (X \mid (SBlk[d] - (X \& SSrc[d])) \& SDst[d]) \quad (2)$$

We explain the behavior of  $\text{Scatter}(X, d)$  for the  $N(v)$  for each  $v \in V[k]$ . Let  $SE(v) = \{(\theta, \varepsilon, \theta_i) \mid 1 \leq i \leq n\}$  for some  $n \geq 1$ . Firstly,  $Y = X \& SSrc[d]$  obtains the bit at the source position  $Bit(\theta)$ , which is the lowest in  $I(v)$ , in the current state set  $X$ .  $SBlk[d]$  is the bitmask that only the highest bit of  $I(v)$  is set to on and the others remain off. Subtracting  $Y$  from  $SBlk[d]$ ,  $Z = SBlk[d] - Y$ , results that all the bits in  $[Bit(\theta) + 1, Bit(\phi) - 1]$  turn on if the source bit  $Bit(\theta)$  of  $Y$  is on, and all bits turn off otherwise. Thus, taking destination mask  $SDst[d]$  and then take bitwise-or to  $X$ ,  $X' = (X \mid (Z \& SDst[d]))$ , turns all bits but the destination bits off. In summary, the above steps correctly copy the source bit in  $X$  to all destination bits in  $X'$ .

(3) *Gather-edge transition*: For  $k \in [0, d]$ , define  $\text{Gather}(X, d)$  by

$$X \leftarrow (X \mid (\text{GBlk}[d] + (X \ \& \ \text{GSrc}[d])) \ \& \ \text{GDst}[d]) \quad (3)$$

Since this is similar to the case for  $\text{Scatter}$  except that  $\text{Gather}$  uses addition, while  $\text{Scatter}$  uses subtraction, we omit the details.

(4) *Mid-edge transition*: For  $k \in [0, d]$ , define  $\text{Mid}(X, d)$  by

$$X \leftarrow (X \mid (X \ll 1) \ \& \ \text{Mid}[k]) \quad (4)$$

For mid edge  $(\phi_i, \varepsilon, \theta_{i+1}) \in \text{ME}(v)$ ,  $\text{Bit}(\theta_{i+1}) = \text{Bit}(\phi_i) + 1$ , and it works correctly.

We can show the next lemma that says that the above bit-parallel method correctly implements  $\alpha$ -transitions and  $\varepsilon$ -transitions of all types necessary for our simulation of *TNFA*.

**Lemma 3.** *Let  $R \in \text{AREG}$  and  $N_R$  be the *TNFA* for  $R$ . For any state set  $S$ , the bitmask  $X = \text{NUM}(\text{BIT}(S))$  for  $S$ , and for any  $1 \leq d \leq d(R)$ : (1)  $X = \text{Alpha}(X, \alpha)$  if and only if  $S = \text{Move}(S, \alpha)$ . (2)  $X = \text{Scatter}(X, d)$  if and only if  $S = \text{Apply}[\text{SE}[d]](S)$ . (3)  $X = \text{Gather}(X, d)$  if and only if  $S = \text{Apply}[\text{GE}[d]](S)$ . (3)  $X = \text{Mid}(X, d)$  if and only if  $S = \text{Apply}[\text{ME}[d]](S)$ . Furthermore, if  $M \leq w$  then the procedures  $\text{Alpha}$ ,  $\text{Scatter}$ ,  $\text{Gather}$ , and  $\text{Mid}$  run in  $O(1)$  time on uniform-cost RAM.*

#### 4.4 Correctness and complexities

Now, we have the algorithm *BPD* of Fig. 7 combined with *RunTNFA* of Fig. 5, *EpsClo* of Fig. 6, and codes for  $\text{Alpha}$ ,  $\text{Scatter}$ ,  $\text{Gather}$ , and  $\text{Mid}$  in the previous subsection. In the followings,  $R \in \text{AREG}$  is an acyclic regular expression with length  $m$  and depth  $d$ ,  $T$  is a text with length  $n$ ,  $M$  is the length of bitmasks, and  $w$  is the bit-width of a computer word.

In the *small automata* case with  $M \leq w$ , we have the main results of this paper from Lemma 1, Lemma 2, and Lemma 3.

**Theorem 1 (case for small automata).** *Suppose that  $m \leq w$ . Given acyclic regular expression  $R \in \text{AREG}$  and input text  $T$  with length  $n$ , The algorithm *BPD* of Fig. 7 solves the regular expression matching problem in  $O(nd)$  time using  $O(md + |\Sigma|)$  preprocessing and  $O(d + |\Sigma|)$  space in words.*

In the case *large automata* case with  $M > w$ , we can show the following theorem by combining our Theorem 1 and Lemma 5 of [4].

**Corollary 1 (case for large automata).** *Suppose that  $m > w$ . For acyclic regular expressions in  $\text{AREG}$ , the regular expression matching problem can be solved in  $O(nmd/w)$  time using  $O(md + m|\Sigma|/w)$  preprocessing and  $O(md/w + m|\Sigma|/w)$  space in words.*

**Corollary 2 (bounded depth).** *For every  $d \geq 0$ , the regular expression matching problem for the class  $\text{AREG}^d$  of acyclic regular expressions with depth  $d$  is solved in  $O(nm/w)$  time using  $O(m)$  preprocessing and  $O(m/w)$  space in words, where  $w$  is the bit-width of a word.*

## 5 Experimental Results

In this section, we give the experimental results for the algorithm *BPD*.

**Experimental settings.** For input texts, we used a DNA sequence data with length 10MB and alphabet size  $|\Sigma| = 4$ , which is a copy of a part of E.coli in the Canterbury Corpus<sup>3</sup>. For patterns, we used a regular expression, called  $(K, L)$ -CNF-Regexp,

<sup>3</sup> <http://corpus.canterbury.ac.nz/>

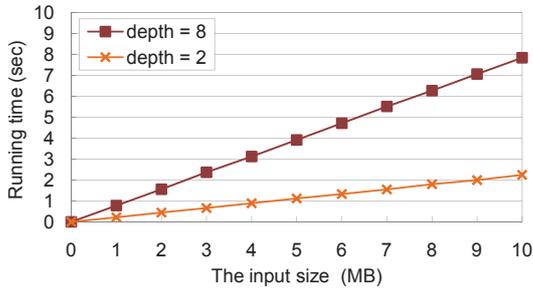


Fig. 10. Running time for the input size.

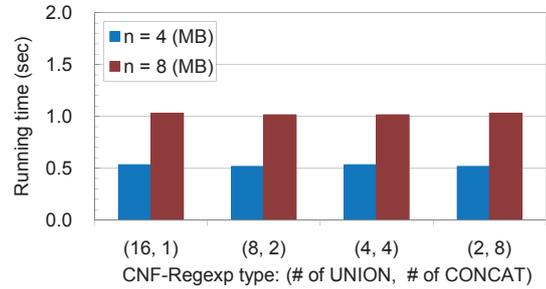


Fig. 11. Running time for CNF-Regex.

with depth  $d = 2$  of the form  $R = (R_1^1 | \dots | R_K^1) \dots (R_1^L | \dots | R_K^L)$  varying parameters by  $(K, L) \in \{(16, 1), (8, 2), (4, 4), (2, 8)\}$ , where each  $R_i^j$  is a string of length 4. The patterns have constant depth  $d$  and almost constant size  $m$  regardless their parameters  $(K, L)$ . We implemented the proposed algorithm *BPD* in C++. All experiments were run on a PC (Intel Core 2 Duo CPU P8600, 2.40 GHz, 32bit words, 4GB RAM, Windows Vista, g++4.3.4 on Cygwin), where an input text is stored on the main memory and the cpu time does not include I/O.

**Experiment A.** Fig. 10 shows the running time of *BPD* by varying the input size  $n = 1 \sim 10$ MB for patterns with depth  $d = 2$  and 8 by explicitly inserting parentheses to the original pattern. Then, the running time seems to be linear in the input size  $n$ , and also, increased with the depth  $d$ .

**Experiment B.** Fig. 11 shows the running time of *BPD* by varying combination of  $(K, L)$  keeping the depth  $d$  and the size  $m$  of  $R$  constant, where the input sizes  $n$  are 4MB and 8MB. Then, the running time seems to be independent of  $(K, L)$  and the shape of patterns when  $d$  and  $m$  are constant. These observations are consistent to theoretical analysis of algorithm *BPD* in Section 4.

## 6 Conclusion

In this paper, we presented a fast matching algorithm for a subclass of the regular expressions. For the subclass, our algorithm *BPD* solves the regular expression matching problem in  $T = O(nmd/w)$  time,  $S = O(md/w)$  space in words, and  $P = O(md)$  preprocessing time with finite alphabet  $\Sigma$  where  $d$  is the depth of regular expression  $w$  is the bit-width of a computer word. If  $d$ ,  $b$ , and  $|\Sigma|$  are constants, *BPD* runs in  $O(nm/w)$  time and  $O(m/w)$  space in words.

In this paper, we considered a restricted subclass AREG of REG, called acyclic regular expressions without backward edges. Unfortunately, the scatter and gather techniques are not directly applicable to TNFA with backward edges. Extension of the result of this paper for subclasses of regular extensions allowing Kleene-plus and Kleene-star is an interesting research topic. This will be discussed elsewhere.

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